

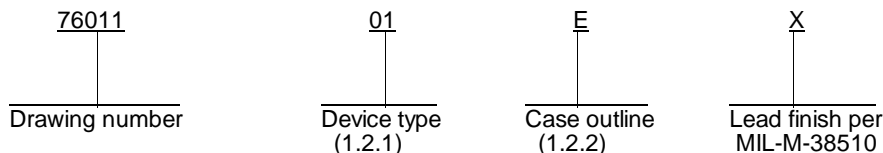
|  |  |  |                                       |
|--|--|--|---------------------------------------|
| <b>NOTICE OF REVISION (NOR)</b><br>(See MIL-STD-480 for instructions)<br>This revision described below has been authorized for the document listed.  |  | DATE (YYMMDD)<br>92-02-21                | Form Approved<br>OMB No.<br>0704-0188 |
| Public reporting burden for this collection is estimated to average 1 hour per response, including the time for reviewing instructions, searching existing data sources, gathering and maintaining the data needed, and completing and reviewing the collection of information. Send comments regarding this burden estimate or any other aspect of this collection of information, including suggestions for reducing this burden, to Washington Headquarters Services, Directorate for Information Operations and Reports, 1215 Jefferson Davis Highway, Suite 1204, Arlington, VA 22202-4302, and to the Office of Information and Regulatory Affairs, Office of Management and Budget, Washington, DC 20503. |  |  |                                       |
| 1. ORIGINATOR NAME AND ADDRESS<br>Defense Electronics Supply Center<br>Dayton, Ohio 45444-5277   |  | 2. CAGE CODE<br>67268                    | 3. NOR NO.<br>5962-R138-92            |
|  |  | 4. CAGE CODE<br>67268                    | 5. DOCUMENT NO.<br>76011              |
| 6. TITLE OF DOCUMENT<br>MICROCIRCUITS, DIGITAL, LOW POWER SCHOTTKY TTL,<br>MULTIPLEXER, MONOLITHIC SILICON.  |  | 7. REVISION LETTER<br>(Current) E        | (New) F                               |
|  |  | 8. ECP NO.<br>CHANGE APPROVED PREVIOUSLY |                                       |
| 9. CONFIGURATION ITEM (OR SYSTEM) TO WHICH ECP APPLIES<br>All  |  |  |                                       |
| 10. DESCRIPTION OF REVISION<br><br>Sheet 1: Revisions ltr column; add "F".<br>Revisions description column; add "Changes in accordance with<br>NOR 5962-R138-92".<br>Revisions date column; add "92-02-21".<br><br>Sheet 2: 1.3 <u>Absolute maximum ratings</u> . Add line "Thermal resistance, Junction to case<br>( $\theta_{JC}$ )" before "Cases E and F".<br><br>Sheet 4: Table I, Supply current, Symbol column, change from "I <sub>CC</sub> " to "I <sub>CCL</sub> ".  |  |  |                                       |
| 11. THIS SECTION FOR GOVERNMENT USE ONLY   |  |  |                                       |
| a. CHECK ONE<br><input checked="" type="checkbox"/> EXISTING DOCUMENT SUPPLEMENTED BY THIS NOR MAY BE USED IN MANUFACTURE. <input type="checkbox"/> REVISED DOCUMENT MUST BE RECEIVED BEFORE MANUFACTURER MAY INCORPORATE THIS CHANGE. <input type="checkbox"/> CUSTODIAN OF MASTER DOCUMENT SHALL MAKE ABOVE REVISION AND FURNISH REVISED DOCUMENT TO:  |  |  |                                       |
| b. ACTIVITY AUTHORIZED TO APPROVE CHANGE FOR GOVERNMENT DESC-ECC   | SIGNATURE AND TITLE<br>Monica Poelking<br>Chief, Custom Microelectronics | DATE (YYMMDD)<br>92-02-21                |                                       |
| 12. ACTIVITY ACCOMPLISHING REVISION<br>DESC-ECC  | REVISION COMPLETED (Signature)   | DATE (YYMMDD)<br>92-02-21                |                                       |



## 1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device type. The device type shall identify the circuit function as follows:

| <u>Device type</u> | <u>Generic number</u> | <u>Circuit</u>                    |
|--------------------|-----------------------|-----------------------------------|
| 01                 | 54LS153               | Dual 4-line to 1-line multiplexer |

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

| <u>Outline letter</u> | <u>Case outline</u>   |
|-----------------------|---|
| E                     | D-2 (16-lead, 1/4" x 7/8"), dual-in-line package            |
| F                     | F-5 (16-lead, 1/4" x 3/8"), flat package                    |
| 2                     | C-2 (20-terminal .350" x .350") square chip carrier package |

1.3 Absolute maximum ratings.

|   |       |                                 |
|---|-------|---------------------------------|
| Supply voltage range                            | ----- | -0.5 to 7.0 V dc                |
| Input voltage range                             | ----- | -1.5 V dc at -18 mA to 5.5 V dc |
| Storage temperature range                       | ----- | -65° C to +150° C               |
| Maximum power dissipation, (P <sub>D</sub> ) 1/ | ----- | 55 mW dc                        |
| Lead temperature (soldering, 10 seconds)        | ----- | +300° C                         |
| Cases E and F                                   | ----- | See MIL-M-38510, appendix C     |
| Case 2  | ----- | 80° C/W 2/                      |
| Junction temperature (T <sub>J</sub> )          | ----- | +175° C                         |

1.4 Recommended operating conditions.

|   |       |                                      |
|---|-------|--------------------------------------|
| Supply voltage (V <sub>CC</sub> )                   | ----- | 4.5 V dc minimum to 5.5 V dc maximum |
| Minimum high-level input voltage (V <sub>IH</sub> ) | ----- | 2.0 V dc                             |
| Maximum low-level input voltage (V <sub>IL</sub> )  | ----- | 0.7 V dc                             |
| Case operating temperature range (T <sub>C</sub> )  | ----- | -55° C to +125° C                    |

1/ Must withstand the added P<sub>D</sub> due to short circuit test (e.g., I<sub>OS</sub>).

2/ When a thermal resistance for this case is specified in MIL-M-38510, appendix C that value shall supersede the value specified herein.

|  |                         |  |                          |
|--|-------------------------|--|--------------------------|
| <b>MILITARY DRAWING</b><br><b>DEFENSE ELECTRONICS SUPPLY CENTER</b><br><b>DAYTON, OHIO 45444</b> | <b>SIZE</b><br><b>A</b> | <b>CODE IDENT. NO.</b><br><b>14933</b> | <b>76011</b>             |
|  |                         | <b>REVISION LEVEL</b><br><b>E</b>      | <b>SHEET</b><br><b>2</b> |

## 2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

### SPECIFICATION

#### MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

### STANDARD

#### MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

## 3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Logic diagram. The logic diagram shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full recommended case operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

|   |                  |                                 |                   |
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TABLE I. Electrical performance characteristics.

| Test  | Symbol            | Conditions<br>-55°C ≤ T <sub>C</sub> ≤ +125°C<br>unless otherwise specified                             |                             | Group A<br>subgroups | Limits |      | Unit |
|---|-------------------|---|-----------------------------|----------------------|--------|------|------|
|   |                   |   |                             |                      | Min    | Max  |      |
| High-level output voltage   | V <sub>OH</sub>   | V <sub>CC</sub> = 4.5 V; V <sub>IL</sub> = 0.7 V;<br>I <sub>OH</sub> = -400 μA; V <sub>IH</sub> = 2.0 V |                             | 1, 2, 3              | 2.5    | ---  | V    |
| Low-level output voltage  | V <sub>OL</sub>   | V <sub>CC</sub> = 4.5 V; V <sub>IH</sub> = 2.0 V;<br>I <sub>OL</sub> = 4 mA; V <sub>IL</sub> = 0.7 V    |                             | 1, 2, 3              | ---    | 0.4  | V    |
| Input clamp voltage   | V <sub>IC</sub>   | V <sub>CC</sub> = 4.5 V; I <sub>IN</sub> = -18 mA;<br>T <sub>C</sub> = +25°C                            |                             | 1                    | ---    | -1.5 | V    |
| High-level input current  | I <sub>IH1</sub>  | V <sub>CC</sub> = 5.5 V; V <sub>IH</sub> = 2.7 V  |                             | 1, 2, 3              | ---    | 20   | μA   |
|   | I <sub>IH2</sub>  | V <sub>CC</sub> = 5.5 V; V <sub>IH</sub> = 5.5 V  |                             | 1, 2, 3              | ---    | 100  | μA   |
| Low-level input current   | I <sub>IL</sub>   | V <sub>CC</sub> = 5.5 V; V <sub>IL</sub> = 0.4 V  |                             | 1, 2, 3              |        | -400 | μA   |
| Short-circuit output current  | I <sub>OS</sub>   | V <sub>CC</sub> = 5.5 V; V <sub>OUT</sub> = 0.0 V <u>1/</u>   |                             | 1, 2, 3              | -6     | -130 | mA   |
| Supply current  | I <sub>CC</sub>   | V <sub>CC</sub> = 5.5 V   |                             | 1, 2, 3              | ---    | 10   | mA   |
| Functional tests  |                   | See 4.3.1c  |                             | 7                    |        |      |      |
| Propagation delay time,<br>high-to-low level <u>2/</u><br>select to Y | t <sub>PHL1</sub> | V <sub>CC</sub> = 5.0 V<br>R <sub>L</sub> = 2 kΩ ±5%  | C <sub>L</sub> = 15 pF ±10% | 9                    | ---    | 38   | ns   |
|   |                   |   |                             | 10, 11               | ---    | 53   | ns   |
|   |                   |   | C <sub>L</sub> = 50 pF ±10% | 9                    | ---    | 43   | ns   |
|   |                   |   |                             | 10, 11               | ---    | 60   | ns   |

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

| Test   | Symbol            | Conditions<br>-55°C ≤ T <sub>C</sub> ≤ +125°C<br>unless otherwise specified |                             | Group A<br>subgroups | Limits |     | Unit |
|--|-------------------|---|-----------------------------|----------------------|--------|-----|------|
|  |                   |   |                             |                      | Min    | Max |      |
| Propagation delay time,<br>low-to-high level <u>2</u> /<br>select to Y | t <sub>PLH1</sub> | V <sub>CC</sub> = 5.0 V<br>R <sub>L</sub> = 2 kΩ ±5%                        | C <sub>L</sub> = 15 pF ±10% | 9                    | ---    | 29  | ns   |
|  |                   |   |                             | 10, 11               | ---    | 41  | ns   |
|  |                   |   | C <sub>L</sub> = 50 pF ±10% | 9                    | ---    | 34  | ns   |
|  |                   |   |                             | 10, 11               | ---    | 48  | ns   |
| Propagation delay time,<br>high-to-low level <u>2</u> /<br>strobe to Y | t <sub>PHL2</sub> |   | C <sub>L</sub> = 15 pF ±10% | 9                    | ---    | 32  | ns   |
|  |                   |   |                             | 10, 11               | ---    | 45  | ns   |
|  |                   |   | C <sub>L</sub> = 50 pF ±10% | 9                    | ---    | 37  | ns   |
|  |                   |   |                             | 10, 11               | ---    | 52  | ns   |
| Propagation delay time,<br>low-to-high level <u>2</u> /<br>strobe to Y | t <sub>PLH2</sub> | C <sub>L</sub> = 15 pF ±10%   | 9                           | ---                  | 24     | ns  |      |
|  |                   |   | 10, 11                      | ---                  | 34     | ns  |      |
|  |                   | C <sub>L</sub> = 50 pF ±10%   | 9                           | ---                  | 29     | ns  |      |
|  |                   |   | 10, 11                      | ---                  | 41     | ns  |      |
| Propagation delay time,<br>high-to-low level <u>2</u> /<br>data to Y   | t <sub>PHL3</sub> | C <sub>L</sub> = 15 pF ±10%   | 9                           | ---                  | 26     | ns  |      |
|  |                   |   | 10, 11                      | ---                  | 36     | ns  |      |
|  |                   | C <sub>L</sub> = 50 pF ±10%   | 9                           | ---                  | 31     | ns  |      |
|  |                   |   | 10, 11                      | ---                  | 43     | ns  |      |

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

| Test  | Symbol            | Conditions<br>-55° C ≤ T <sub>C</sub> ≤ +125° C<br>unless otherwise specified |                             | Group A<br>subgroups | Limits |     | Unit |
|---|-------------------|---|-----------------------------|----------------------|--------|-----|------|
|   |                   |   |                             |                      | Min    | Max |      |
| Propagation delay time,<br>low-to-high level <u>2/</u><br>data to Y | t <sub>PLH3</sub> | V <sub>CC</sub> = 5.0 V<br>R <sub>L</sub> = 2 kΩ ±5%                          | C <sub>L</sub> = 15 pF ±10% | 9                    | ---    | 15  | ns   |
|   |                   |   |                             | 10, 11               | ---    | 21  | ns   |
|   |                   |   | C <sub>L</sub> = 50 pF ±10% | 9                    | ---    | 20  | ns   |
|   |                   |   |                             | 10, 11               | ---    | 28  | ns   |

1/ Not more than 1 output should be shorted at a time, and the duration of the short-circuit condition should not exceed 1 second.

2/ Propagation delay time testing may be performed using either  $C_L = 15\text{ pF}$  or  $C_L = 50\text{ pF}$ . However, the manufacturer must certify and guarantee that the microcircuits meet the switching test limits specified for a 50 pF load.

3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

#### 4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test (method 1015 of MIL-STD-883).

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2)  $T_A = +125^{\circ}\text{C}$ , minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

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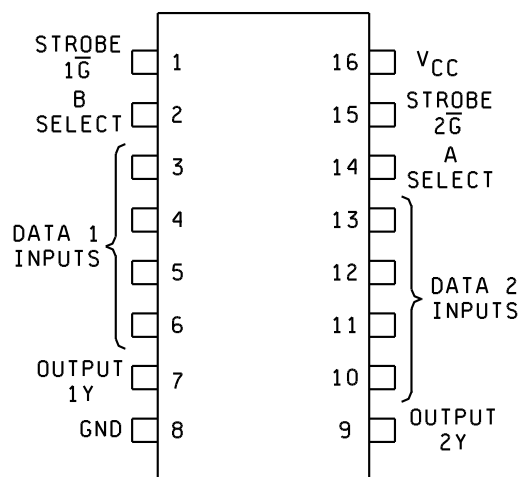
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# CASES E AND F



## CASE 2

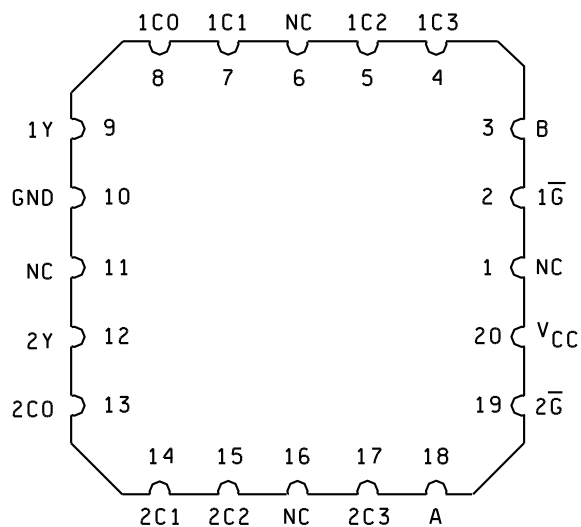


FIGURE 1. Terminal connections.

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| Select Inputs |   | Data inputs |    |    |    | Strobe    | Output |
|---------------|---|-------------|----|----|----|-----------|--------|
| B             | A | CO          | C1 | C2 | C3 | $\bar{G}$ | Y      |
| X             | X | X           | X  | X  | X  | H         | L      |
| L             | L | L           | X  | X  | X  | L         | L      |
| L             | L | H           | X  | X  | X  | L         | H      |
| L             | H | X           | L  | X  | X  | L         | L      |
| L             | H | X           | H  | X  | X  | L         | H      |
| H             | L | X           | X  | L  | X  | L         | L      |
| H             | L | X           | X  | H  | X  | L         | H      |
| H             | H | X           | X  | X  | L  | L         | L      |
| H             | H | X           | X  | X  | H  | L         | H      |

Select inputs A and B are common to both sections.  
H = high level, L = low level, X = irrelevant.

FIGURE 2. Truth table.

|   |                  |                                 |                   |
|---|------------------|---------------------------------|-------------------|
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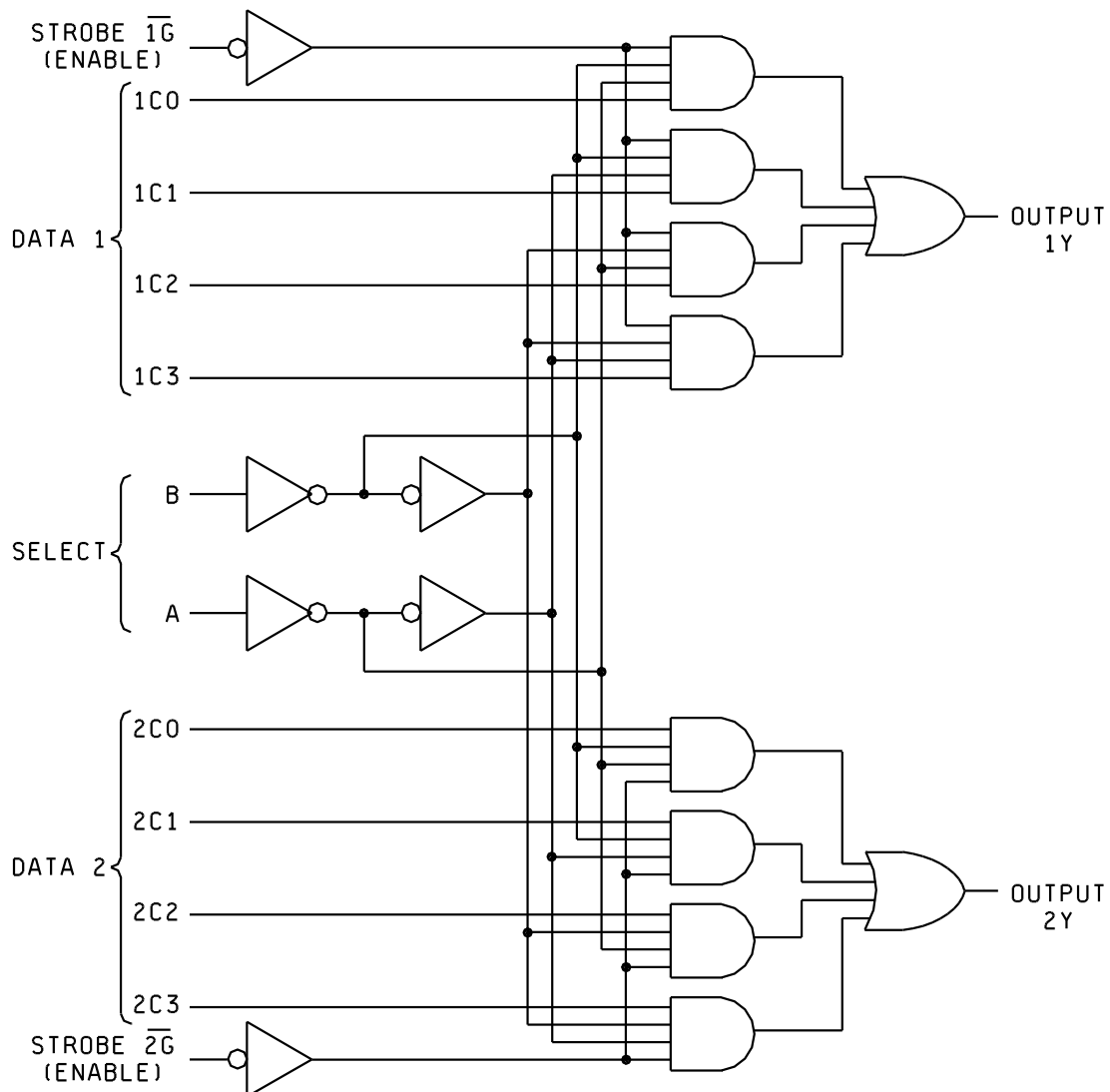


FIGURE 3. Logic diagram.

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4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.
- c. Subgroup 7 tests shall verify the truth table.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test (method 1005 of MIL-STD-883) conditions:
  - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
  - (2)  $T_A = +125^{\circ}\text{C}$ , minimum.
  - (3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.

TABLE II. Electrical test requirements.

| MIL-STD-883 test requirements                                      | Subgroups<br>(per method<br>5005, table I) |
|--|--|
| Initial electrical parameters<br>(method 5004)                     | ---  |
| Final electrical test parameters<br>(method 5004)                  | 1*, 2, 3, 9                                |
| Group A test requirements<br>(method 5005)                         | 1, 2, 3, 7, 9<br>10, 11**                  |
| Groups C and D end-point<br>electrical parameters<br>(method 5005) | 1, 2, 3                                    |

\* PDA applies to subgroup 1.

\*\* Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

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## 6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Replaceability is determined as follows:

- a. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
- b. When a QPL source is established, the part numbered device specified in this drawing will be replaced by the microcircuit identified as part number M38510/30902B--.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

6.4 Approved sources of supply. Approved sources of supply are listed herein. Additional sources will be added as they become available. The vendors listed herein have agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

| Military drawing part number | Vendor CAGE number | Vendor similar part number <u>1/</u> | Replacement military specification part number |
|------------------------------|--------------------|--------------------------------------|--|
| 7601101EX <u>2/</u>          | 01295<br>04713     | SNJ54LS153J<br>54LS153BEXJC          | M38510/30902BEX                                |
| 7601101FX <u>2/</u>          | 01295<br>04713     | SNJ54LS153W<br>54LS153BFXJC          | M38510/30902BFX                                |
| 76011012X <u>2/</u>          | 01295<br>04713     | SNJ54LS153FK<br>54LS153M/B2XJC       | M38510/30902B2X                                |

1/ Caution. Do not use this number for item acquisition. Items acquired to the similar vendor type only may not satisfy the performance requirements of this drawing.

2/ Inactive for new design. Use QPL-38510 product.

Vendor CAGE  
number

01295

04713

Vendor name  
and address

Texas Instruments, Incorporated  
P.O. Box 6448  
Midland, TX 79701

Motorola, Incorporated  
7402 S. Price Road  
Tempe, AZ 85283

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